



PLL Synthesizer Module

Features

· 4dBm Output Level at 1635MHz

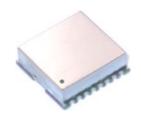
· 2nd Harmonic: < -20dBc

· Spurious Level: < -68dBc

· Current Consumption: < 40mA

Description

The plerowTM PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz		1635	
Output Power	dBm	2	4	6
Supply Voltage	V	4.8	5	5.2
Current Consumption	mA		27	40
2 nd Harmonics	dBc		-25	-20
Spurious Level	dBc		-72	-68
Reference Frequency	MHz		13	
Reference Input Level	dBm	0	2	5
Phase Noise (C / N)				
@ 1 kHz			-76	-72
@ 10 kHz	dBc/Hz		-103	-99
@ 100 kHz			-120	-115
Output Impedance	Ω		50	
Operating Temp. Range	°C	-30	25	80
Package Type & Size	mm	SMT, 19.0W×19.0L×5.8H		

¹⁾ Measurement conditions are as follows: T = 25°C, $V_{CC} = 5$ V, Freq. = 1635MHz, 50 ohm system.

More Information

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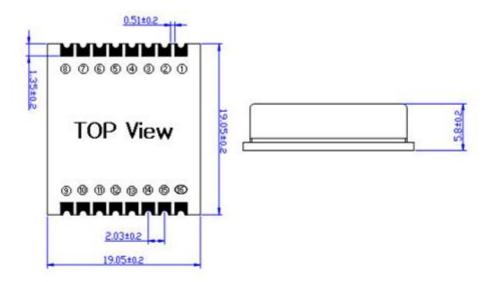
²⁾ Frequency stability of internal ref. TCXO is \pm 2.5 ppm (max.) over the operating temperature range.



Outline Drawing

APL0000-R/T Type

Туре	Dimension	
PLL IC + VCO + ROM + TCXO	19 x 19 x 5.8	



Pin Out for PLL					
Pin No.	Application	Pin No.	Application		
1	GROUND	9	VCC (VCO)		
2	GROUND	13	RF OUT		
3	GROUND	15	VCC (PLL)		
4	GROUND	16	LOCK DETECT		

