





Features

· +5 dBm Output Level at 760 MHz

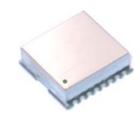
· 2nd Harmonic: < -20 dBc

· Spurious Level: < -70 dBc

· 25 mA Current Consumption

Description

The plerowTM PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz		760	
Output Power	dBm	3	5	7
Supply Voltage	V	4.7	5	5.3
Current Consumption	mA		25	35
2 nd Harmonics	dBc		-25	-20
Spurious Level	dBc		-78	-70
Reference Frequency	MHz		10	
Reference Input Level	dBm	3	5	
Phase Noise (C / N)				
@ 1 kHz				
@ 10 kHz	dBc/Hz		-106	-102
@ 100 kHz			-120	-115
Output Impedance	Ω		50	
Operating Temp. Range	°C	-40	25	85
Package Type & Size	mm	SMT, 19.0W×19.0L×5.8H		

¹⁾ Measurement conditions are as follows: T = 25°C, V_{CC} = 5 V, Freq. = 760 MHz, 50 ohm system.

More Information

Website: www.asb.co.kr E-mail: sales@asb.co.kr

Tel: (82) 42-528-7220 Fax: (82) 42-528-7222

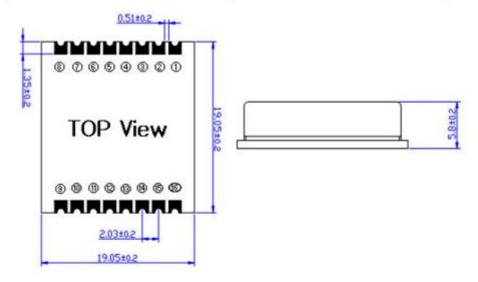
ASB Inc., 4th Fl. Venture Town Bldg., 367-17 Goijeong-Dong, Seo-Gu, Daejeon 302-716, Ko-

1/2 www.asb.co.kr December 2010

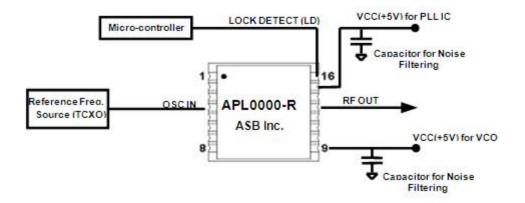


APL0000-R Type

Туре	Dimension	
PLL IC + VCO + ROM	19 x 19 x 5.8	



Pin Out for PLL					
Pin No.	Application	Pin No.	Application		
1	GROUND	9	VCC (VCO)		
2	GROUND	13	RFOUT		
3	GROUND	15	VCC (PLL)		
4	OSC IN	16	LOCK DETECT		



2/2 <u>www.asb.co.kr</u> December 2010