OUTPUT	s				
	Frequency		Level (into 50Ω)		
A	10 MHz		+13 ±2 dBm		
В	100 MHz		+13 ±2 dBm		
С	500 MHz		+13 ±2 dBm		
D	1 GHz		+13 ±2 dBm		
STABILI			+13 ±2 ubiii		
Aging	II				
	⁻⁷ first yea	r			
after 30 days operating, typical					
5 x 10 ⁻⁸ second year, typical					
			fter, typical		
Phase No	oise L(f), o				
10 Hz	10 MHZ -140	-120	Hz 500 MHz -105	-99	
100 Hz	-160	-138		-117	
300 Hz	-165	-144		-122	
1 kHz	-172	-157		-135	
10 kHz				-152	
	-175	-176	-161	-154	
±5 x 1 Harmoni	cs		ef. +25°C)		
≤ -25 dBc Sub-Harmonics					
≤ - 60 (
PLL Reference Products					
≤ -60 dBc					
Spurious ≤ -80 dBc, excluding power					
supply line related spurs					
Phase Lock Alarm					
TTL					
Locked: +3.5 VDC to +5.2 VDC (Hi)					
Out-of-Lock: +0.8 VDC max (Lo)					
Phase Lock Voltage Monitor					
Voltage monitor pin supplied					
MECHANICAL					
Dimensions 6.51 x 4 x 1"					
0.51 X 4 X 1					

Connectors
RF Outputs: SMA(f)
Power, Monitoring: Feed Thru
Terminals
GND: Ground Turret
Packaging
Nickel-plated machined
aluminum housing – J2PMX-013
Mounting
Threaded inserts on base,
#2-56, 11 places
POWER REQUIREMENTS
Warm-Up Power
≤ 20.5 Watts for 5 minutes
Total Power
≤ 13.5 Watts at +25°C
Supply Voltage
+15 VDC ±5%
ADJUSTMENT
Mechanical Tuning (Internal 10 MHz)
$\pm 1 \times 10^{-6}$
Loop BW (Internal 100 MHz PLL)
Target Bandwidth: ~300 Hz
Type 2 Loop
CRYSTAL
Туре
100 MHz SC-cut (x10)
OTHER
Label
Use conventional label with the
following information:
501-25938 (Current Rev.)
10M/100M/500M/1GHz MXO-PLM
+15 VDC
Serial # - Date Code
(Mark connectors with function)
Test Data
- Output Level
- Phase Noise
- Temperature Stability
- Harmonics, Subs, Products, Spurs
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- narmonics, Subs, Products, Spurs
- Power Warm-up and Total

