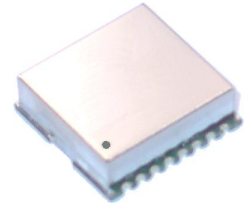


## Features

- -5 dBm Output Level at 827.5 MHz
- Channel Step Size: 250 kHz
- 2<sup>nd</sup> Harmonic: < -20 dBc
- Spurious Level: < -65 dBc
- Current Consumption: <35 mA

## Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



## Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz		827.5	
Output Power	dBm	-7	-5	-3
Supply Voltage	V	4.8	5	5.2
Current Consumption	mA		25	35
Channel Step Size	kHz		250	
2 <sup>nd</sup> Harmonics	dBc		-25	-20
Spurious Level	dBc		-70	-65
Reference Frequency	MHz		10	
Reference Input Level	dBm	0	2	5
Phase Noise (C / N)				
@ 1 kHz			-75	-70
@ 10 kHz	dBc/Hz		-103	-100
@ 100 kHz			-119	-115
Output Impedance	Ω		50	
Operating Temp. Range	°C	-30	25	80
Package Type & Size	mm	SMT, 15.0W×15.0L×3.5H		

1) Measurement conditions are as follows: T = 25°C, V<sub>CC</sub> = 5 V, Freq. = 827.5 MHz, 50 ohm system.

## More Information

Website: [www.asb.co.kr](http://www.asb.co.kr)  
E-mail: [sales@asb.co.kr](mailto:sales@asb.co.kr)

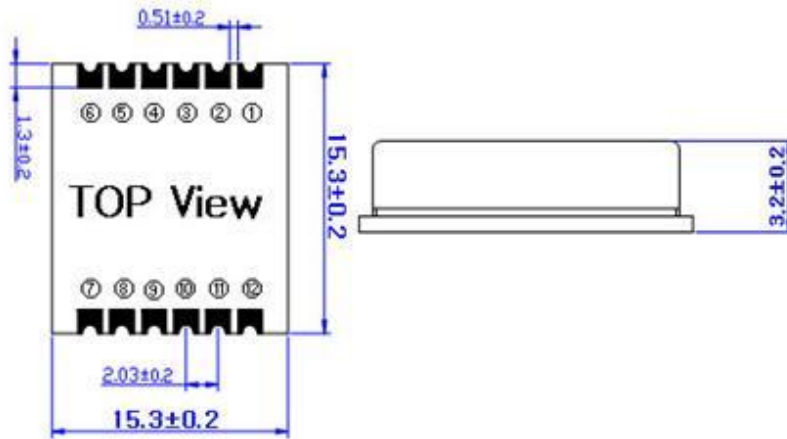
Tel: (82) 42-528-7220  
Fax: (82) 42-528-7222

ASB Inc., 4th Fl. Venture Town  
Bldg., 367-17 Goijeong-Dong,  
Seo-Gu, Daejeon 302-716, Korea

Outline Drawing

APLT0000-R Type

Type	Dimension
PLL IC + VCO + ROM	15.3 x 15.3 x 3.2



Pin Out for PLL			
Pin No.	Application	Pin No.	Application
1	GROUND	7	VCC (VCO)
2	GROUND	10	RF OUT
3	GROUND	11	VCC (PLL)
4	OSC IN	12	LOCK DETECT
All other Pins are Grounded / Internal ROM			

