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SF1143B-4

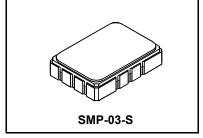
- Designed for SDARS IF Receiver
- · Low Insertion Loss
- 5.0 X 7.0 mm Surface-Mount Case
- · Differential Input and Output
- Complies with Directive 2002/95/EC (RoHS)



Absolute Maximum Ratings

Aboolato maximum ratingo				
Rating	Value	Units		
Maximum Incident Power in Passband	+10	dBm		
Max. DC voltage between any 2 terminals	30	VDC		
Storage Temperature Range -40 t		°C		
Max Soldering Profile	265°C	for 10 s		

315.00 MHz **SAW Filter**



Electrical Characteristics

Characteristic		Sym	Notes	Min	Тур	Max	Units
Nominal Center Frequency		f _C	1	315.000 MI			MHz
Passband Insertion Loss at fc		IL	'		15.1	17.0	dB
	1dB Passband	BW ₁		±6.35	±7.05		MHz
	Fast Amplitude Ripple over fc ±6.35 MHz		1, 2			1.5	dB _{P-P}
	Group Delay Variation over fc ±6.35 MHz	GDV			23	200	ns _{P-P}
Rejection	100 to fc-10.3 and fc+10.3 to fc+100 MHz		1, 2, 3	38	45		dB
Operating Temperature Range		T _A	1	-40		+85	°C
Differential Input and Output Impedance		250 ohms					
Case Style		6 SMP-03-S 7 x 5 mm Nominal Footprint				tprint	
Lid Symbolization (YY=year, WW=week, S=shift) See note 4			, o		RFM SF1143	B-4 YYWWS	

Electrical Connections

Connection	Port 1 Hot	Port 1 Ground Return or Hot	Port 2 Hot	Port 2 Ground Return or Hot	Case Ground
Terminals	10	1	5	6	All Others



CAUTION: Electrostatic Sensitive Device. Observe precautions for handling.

NOTES:

- Unless noted otherwise, all specifications apply over the operating temperature range with filter soldered to the specified demonstration board with impedance matching to 50 Ω and measured with 50 Ω network analyzer.
- Unless noted otherwise, all frequency specifications are referenced to the nominal center frequency, fc.
- Rejection is measured as attenuation below the minimum IL point in the passband. Rejection in final user application is dependent on PCB layout and external impedance matching design. See Application Note No. 42 for details.

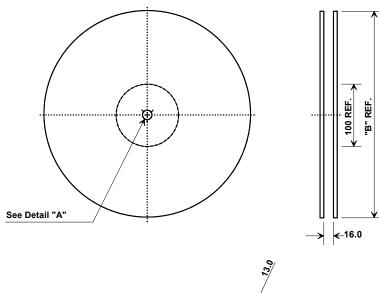
 "LRIP" or "L" after the part number indicates "low rate initial production" and "ENG" or "E" indicates "engineering prototypes."

The design, manufacturing process, and specifications of this filter are subject to change. Tape and Reel Standard ANSI / EIA 481.

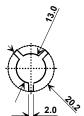
- Either Port 1 or Port 2 may be used for either input or output in the design. However, impedances and impedance matching may vary between Port 1 and Port 2, so that the filter must always be installed in one direction per the circuit design.
- 8.
- US and international patents may apply.

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Tape and Reel Specifications

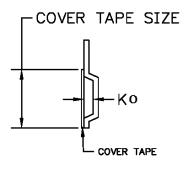


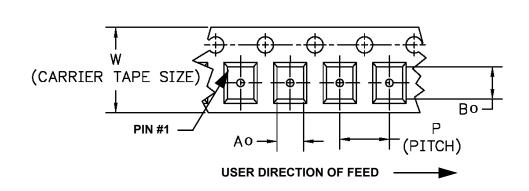
"B " Nominal Size		Quantity Per Reel			
Inches millimeters					
7	178	500			
13	330	2000			



COMPONENT ORIENTATION and DIMENSIONS

Carrier Tape Dimensio	Tolerance	
Ao	5.5 mm	± 0.1mm
Во	7.5 mm	± 0.1mm
Ко	2.0 mm	± 0.1mm
Pitch	8.0 mm	± 0.1mm
W	16.0 mm	± 0.2mm

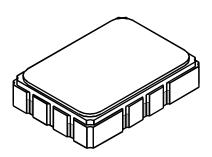




SMP-03-S Case



12-Terminal Ceramic Surface-Mount Case 5 x 7 mm Nominal Footprint



Case Dimensions						
Dimension	mm		Inches			
	Min	Nom	Max	Min	Nom	Max
Α	6.80	7.00	7.20	0.268	0.276	0.283
В	4.80	5.00	5.20	0.189	0.197	0.205
С		1.65	2.00		0.065	0.079
D		0.80				
E	2.41	2.54	2.67	0.095	0.100	0.105
Н	0.87	1.1	1.13	0.034	0.039	0.044
J		2.54				
K	2.87	3.00	3.13	0.113	0.118	0.123
Р	1.14	1.27	1.40	0.045	0.050	0.055

Materials		
Solder Pad	Au plating 30 - 60 μinches (76.2-152 μm) over 80-	
Termination	200 μinches (203-508 μm) Ni.	
Lid	Fe-Ni-Co Alloy Electroless Nickel Plate (8-11% Phos-	
	phorus) 100-200 µinches Thick	
Body	Al ₂ O ₃ Ceramic	
Pb Free		

