



Product Features

- 50 ~ 3500MHz
- GaAs E-pHEMT MMIC
- 0.5W Medium Amplifier
- Higher linearity
- Low Noise Figure
- High Max input power
- SOT-89 SMD Type package
- Higher productivity
- Lower manufacturing cost
- Pb Free / RoHS Standard

Applications

- Cellular, GSM
- PCS, DCS, W-CDMA
- Wibro, WiMax, WiFi
- Tetra, CATV, Satellite system
- RFID, Femtocell
- Multi-metering



Package Type : SOT-89

Description

AE367 is a drive or pre-drive amplifier designed in a low cost SOT-89 package.

This MMIC is based on Gallium Arsenide Enhancement Mode pHEMT which shows low current and high IP3.

It is designed as driver devices for infrastructure equipment in the 50~3500MHz Wireless technologies such as Cellular, GSM, PCS, W-CDMA, Wibro, WiMax System.

The data in this spec sheet is valid only for 50 ohm application.

Electrical Specifications

PARAMETER	UNIT	Test Frequency			REMARK
		1960MHz	2140MHz	2600MHz	
Small Signal Gain	dB	16.2	15.5	14.2	-
Input Return Loss	dB	-15	-15	-15	-
Output Return Loss	dB	-13	-13.5	-17	-
Output IP3*	dBm	39	39	38.5	-
1dB Compression Point	dBm	27	27	27.5	-
Noise Figure	dB	3.5	3.5	3.2	-
WCDMA**	dBm	18.5	18.5	19	@ -50dBc ACLR
Efficiency	%	50	50	52	@ P1dB
Supply Voltage/DC Current	V/mA	5/140			-

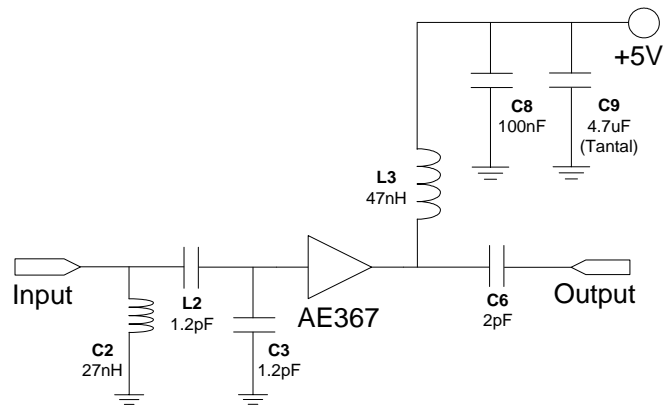
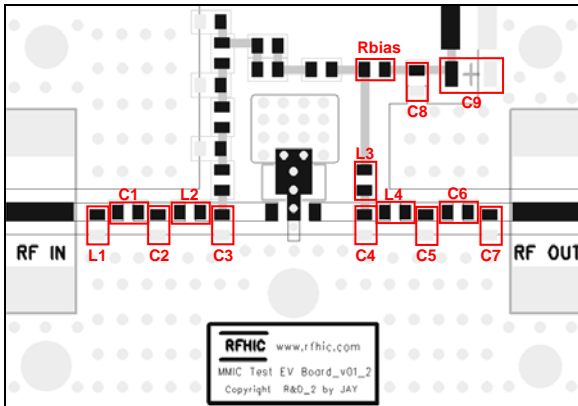
Note

1. OIP3 measured with 2 tones at an output power of +10dBm/tone separated by 1MHz
2. WCDMA 3G 1FA Test Model1 64DPCH, PAR = 10dB@0.01%

Absolute Maximum Ratings

PARAMETER	UNIT	MIN	TYP	MAX	REMARK
Device Voltage	V	-	5	7	-
Operating Case Temperature	°C	-40	-	85	-
Storage Temperature	°C	-40	-	150	-
ESD Human Body Model	-	-	Class 1B	-	-
Moisture Sensitivity Level	-	-	MSL1	-	-
Junction Temperature (Tj)	°C	-	-	180	@ quiescent current,
Thermal Resistance (Rth)	°C/W	-	62	-	No RF, Tc = 85 °C

Application Circuit @ 1930 ~ 1990MHz, 50ohm System



EVB BOM			
Description	Reference Designator	Manufacturer	Manufacturer's P/N
CAP, 1.2pF, 1608	L2, C3	Murata	GRM1885C1H1R2CZ01D
CAP, 2pF, 1608	C6	Murata	GRM1885C1H2R0CZ01D
CAP, 100nF, 1608	C8	Murata	GRM188R71C104KA01D
CAP, 4.7uF, 3216-18	C9	AVX	TAJA475M016RNJ
IND, 27nH, 1608	C2	Taiyo Yuden	HK160827NJ-T
IND, 47nH, ±5%, 1608, W/W	L3	Murata	LQW18AN47NJ00
RES, 00hm, 1608	C1, Rbias, L4	ROHM	MCR03 EZPJ000
DNP	L1, C4, C5, C7	-	-

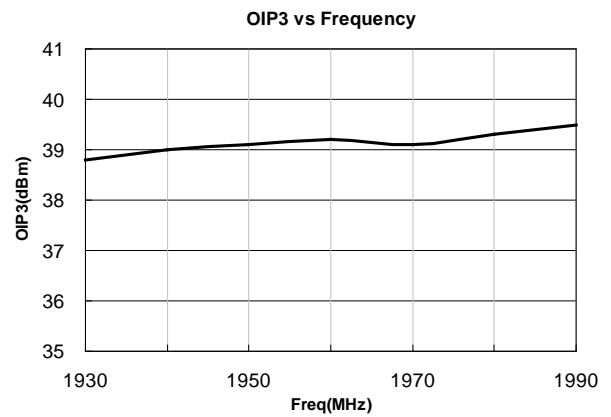
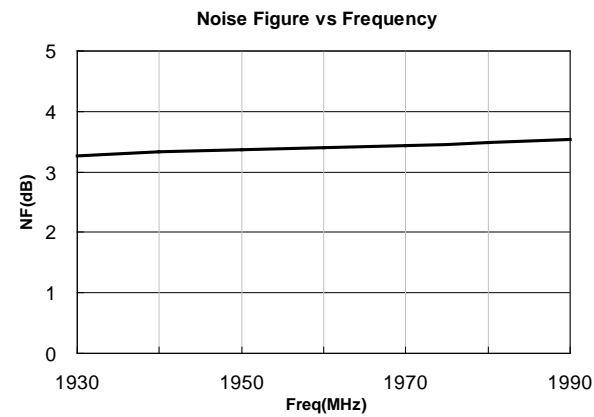
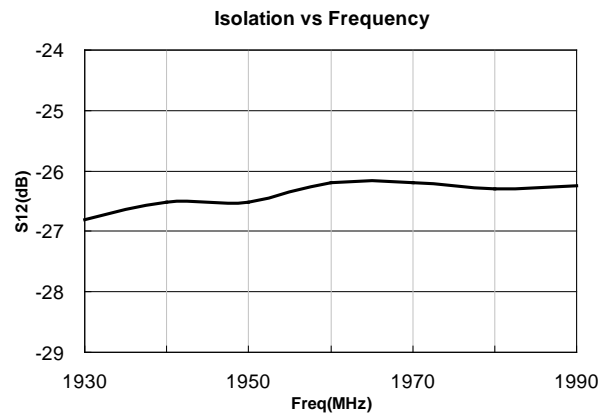
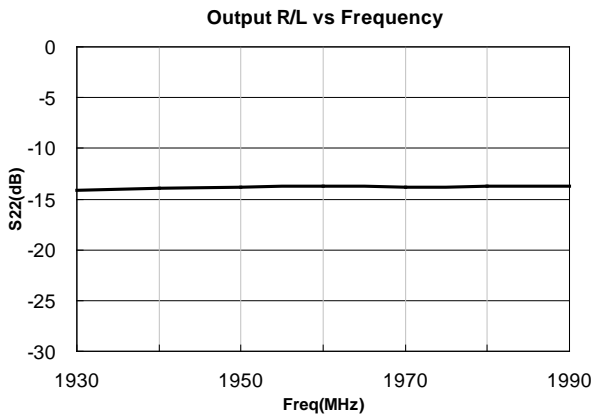
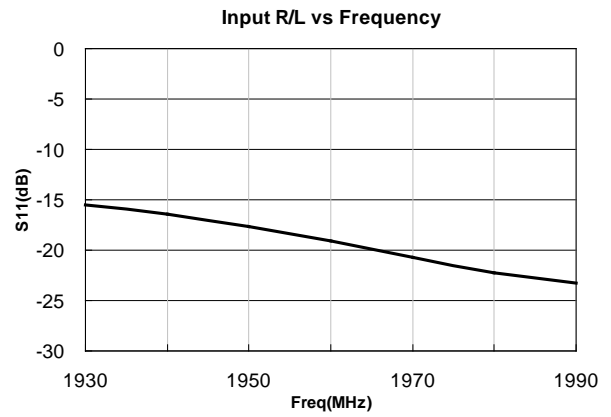
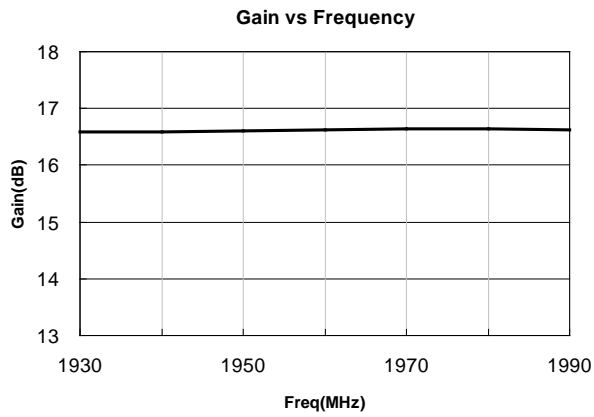
Typical Performance

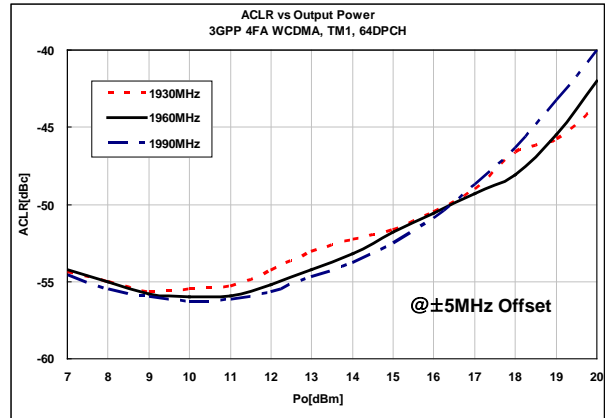
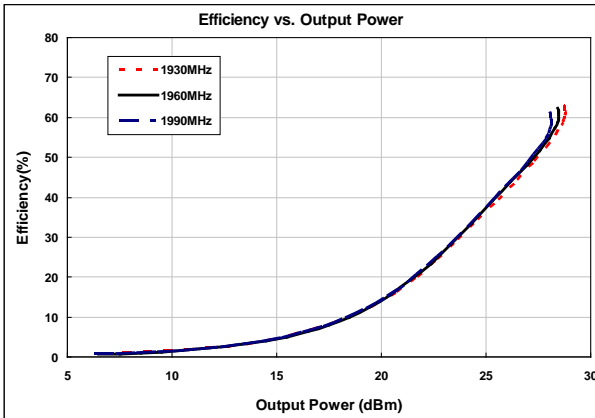
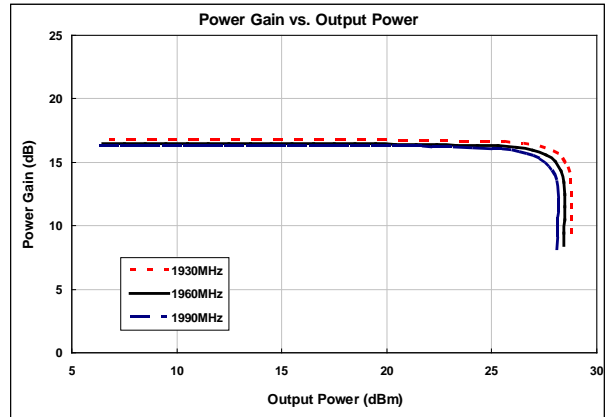
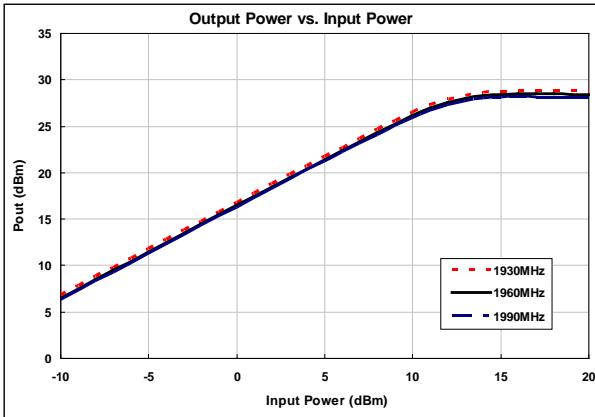
PARAMETER	UNIT	TYPICAL	REMARK
Operational Frequency Range	MHz	1930 ~ 1990	
Small Signal Gain(S21)	dB	16.2	@1960MHz
Input Return Loss(S11)	dB	-15	-
Output Return Loss(S22)	dB	-13	-
Output IP3(OIP3)	dBm	39	@ 1960MHz
1dB Compression Point(P ₁ dB)	dBm	27	
Noise Figure(NF)	dB	3.5	-
WCDMA**	dBm	18.5	-
Efficiency	%	50	-
Supply Voltage/DC Current	V/mA	5/140	-

Note

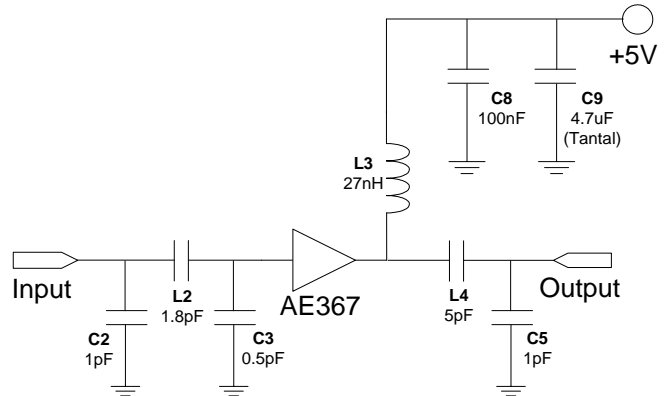
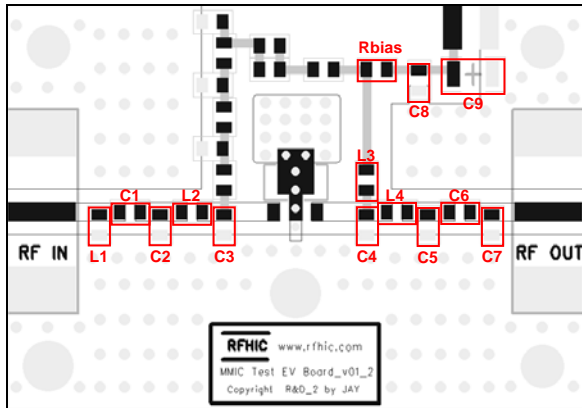
- OIP3 measured with 2 tones at an output power of +10dBm/tone separated by 1MHz
- WCDMA 3G 1FA Test Model1 64DPCH, PAR = 10dB@0.01%

Typical Performance @ $V_{DD}=5V$, $I_{DS}=140mA$, $T=25^{\circ}C$, 50ohm System





Application Circuit @ 2110 ~ 2170MHz, 50ohm System



EVB BOM			
Description	Reference Designator	Manufacturer	Manufacturer's P/N
CAP, 0.5pF, 1608	C3	Murata	GRM1885C1HR50CZ01D
CAP, 1pF, 1608	C2, C5	Murata	GRM1885C1H1R0CZ01D
CAP, 1.8pF, 1608	L2	Murata	GRM1885C1H1R8CZ01D
CAP, 5pF, 1608	L4	Murata	GRM1885C1H5R0CZ01D
CAP, 100nF, 1608	C8	Murata	GRM188R71C104KA01D
CAP, 4.7uF, 3216-18	C9	AVX	TAJA475M016RNJ
IND, 27nH, ±5%, 1608, W/W	L3	Murata	LQW18AN27NJ00
RES, 00ohm, 1608	C1, Rbias, C6	ROHM	MCR03 EZPJ000
DNP	L1, C4, C7	-	-

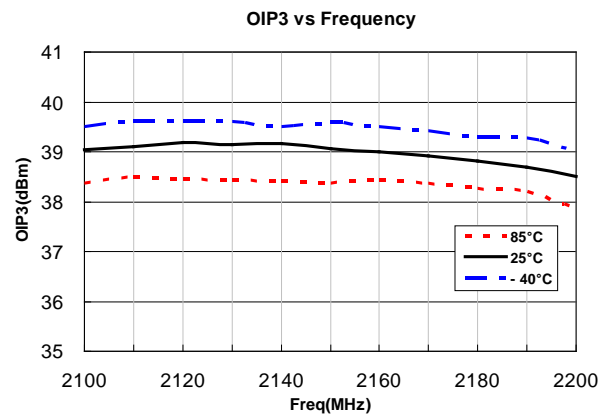
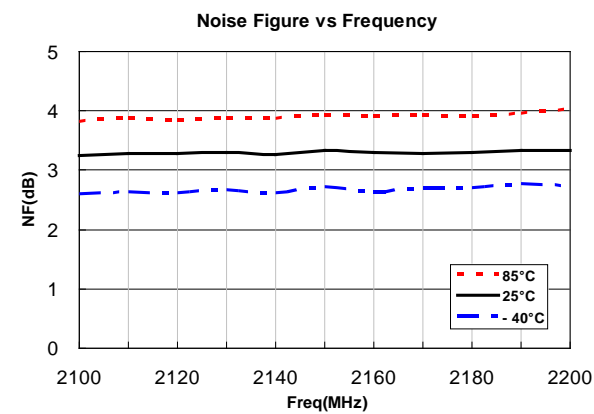
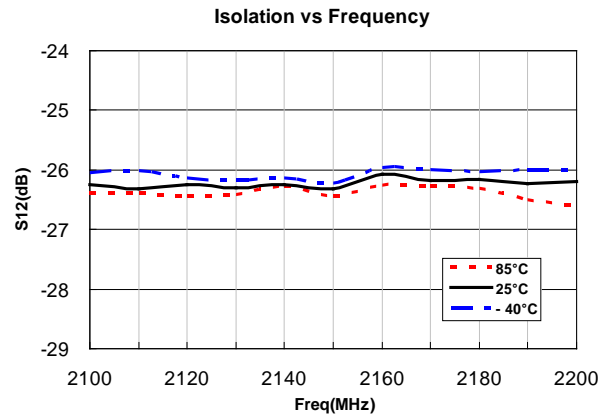
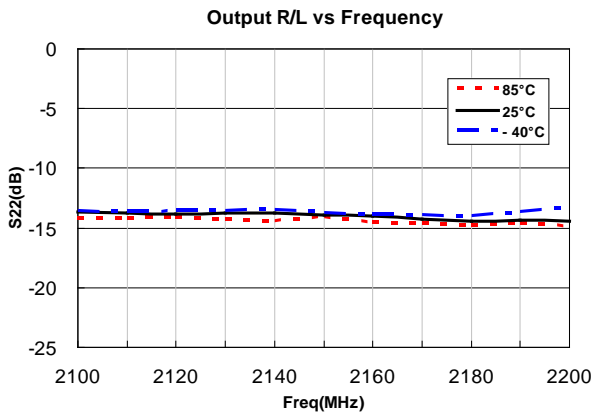
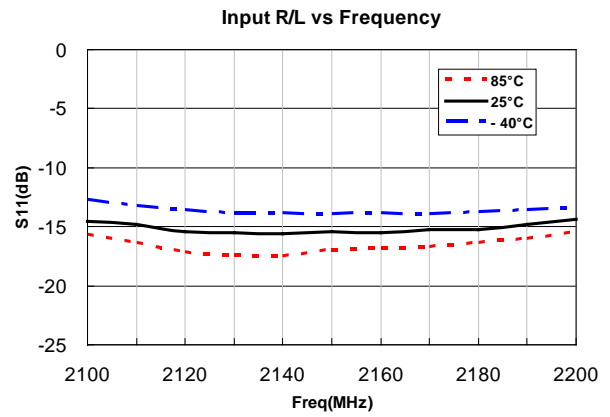
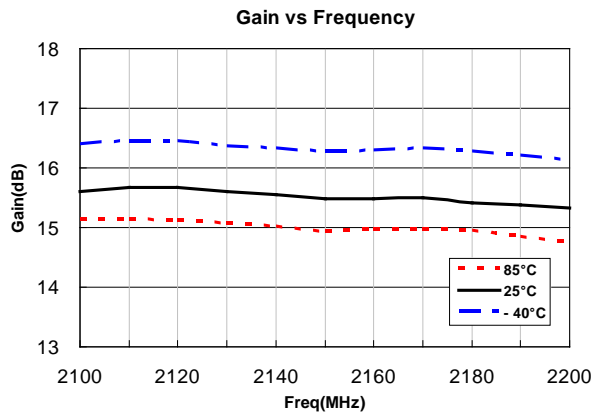
Typical Performance

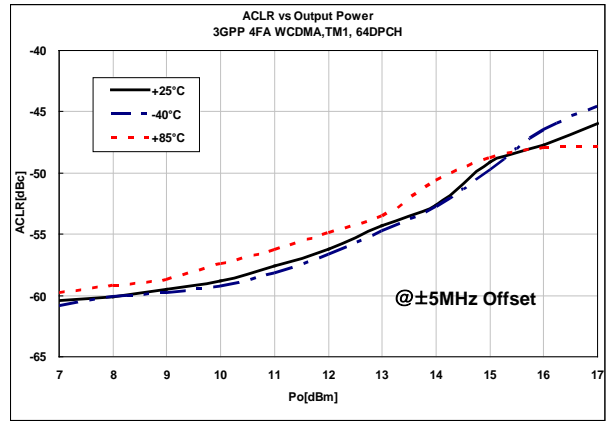
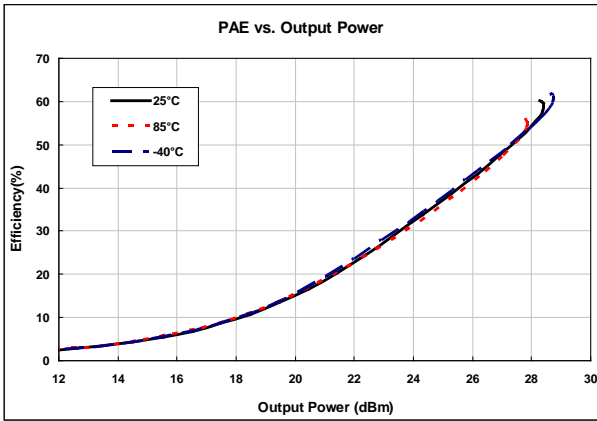
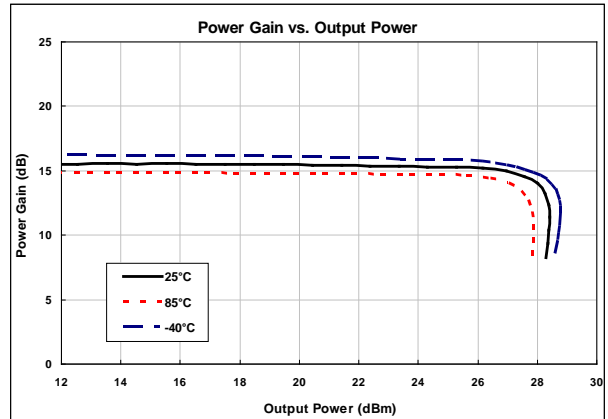
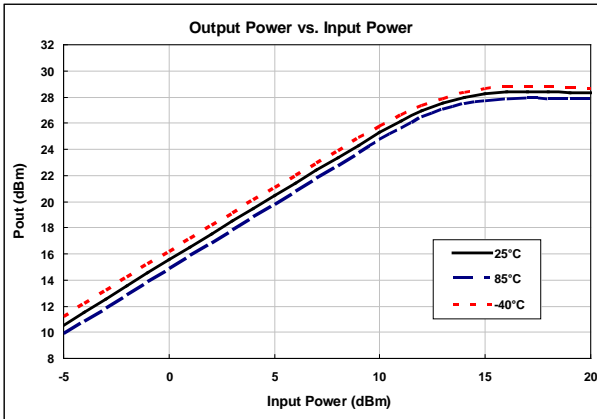
PARAMETER	UNIT	TYPICAL	REMARK
Operational Frequency Range	MHz	2110 ~ 2170	-
Small Signal Gain(S21)	dB	15.5	@ 2140MHz
Input Return Loss(S11)	dB	-15	-
Output Return Loss(S22)	dB	-13.5	-
Output IP3(OIP3)	dBm	39	@ 2140MHz
1dB Compression Point(P1dB)	dBm	27	
Noise Figure(NF)	dB	3.5	-
WCDMA**	dBm	18.5	-
Efficiency	%	50	-
Supply Voltage/DC Current	V/mA	5/140	-

Note

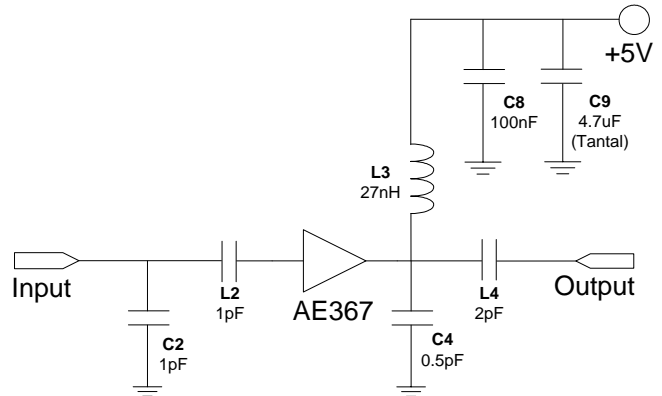
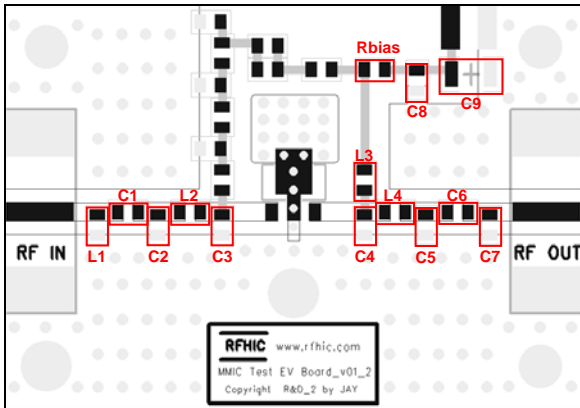
- OIP3 measured with 2 tones at an output power of +10dBm/tone separated by 1MHz
- WCDMA 3G 1FA Test Model1 64DPCH, PAR = 10dB@0.01%

Typical Performance @ $V_{DD}=5V$, $I_{DS}=140mA$, 50ohm System





Application Circuit @ 2550 ~ 2650MHz, 50ohm System



EVB BOM			
Description	Reference Designator	Manufacturer	Manufacturer's P/N
CAP, 0.5 pF, 1608	C4	Murata	GRM1885C1HR5CZ01D
CAP, 1pF, 1608	C2, L2	Murata	GRM1885C1H1R0CZ01D
CAP, 2pF, 1608	L4	Murata	GRM1885C1H2R0CZ01D
CAP, 100nF, 1608	C8	Murata	GRM188R71C104KA01D
CAP, 4.7uF, 3216-18	C9	AVX	TAJA475M016RNJ
IND, 27nH, ±5%, 1608, W/W	L3	Murata	LQW18AN27NJ00
RES, 00hm, 1608	C1, Rbias, C6	ROHM	MCR03 EZPJ000
DNP	L1, C3, C5, C7	-	-

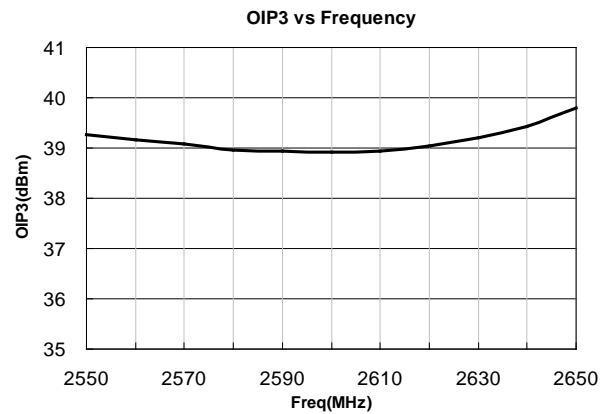
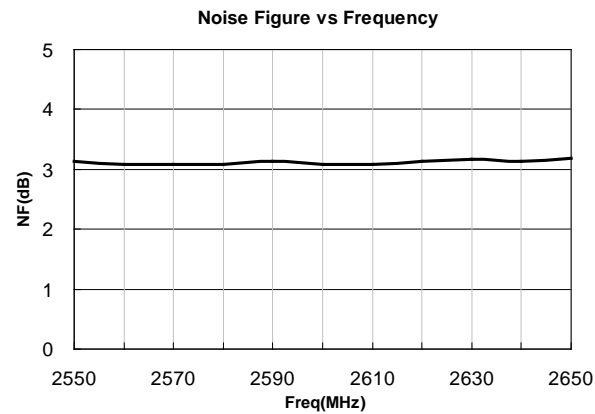
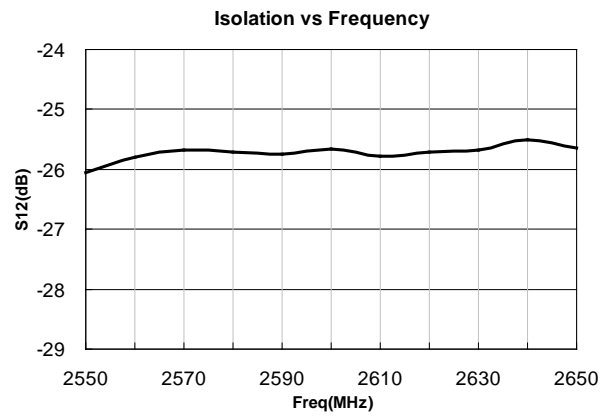
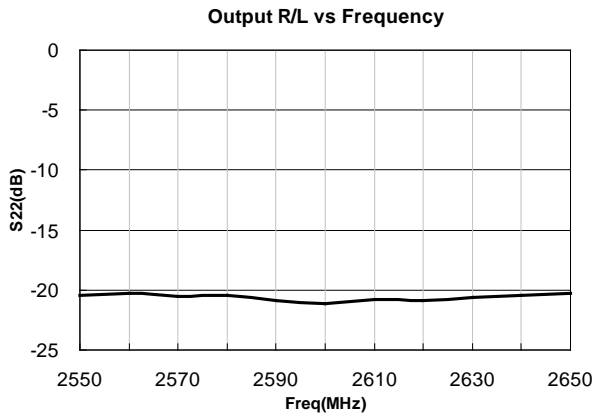
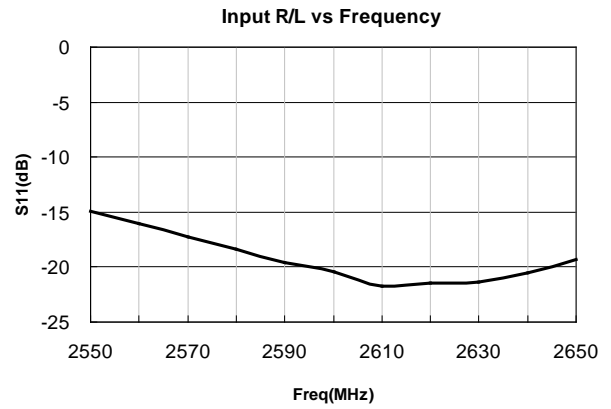
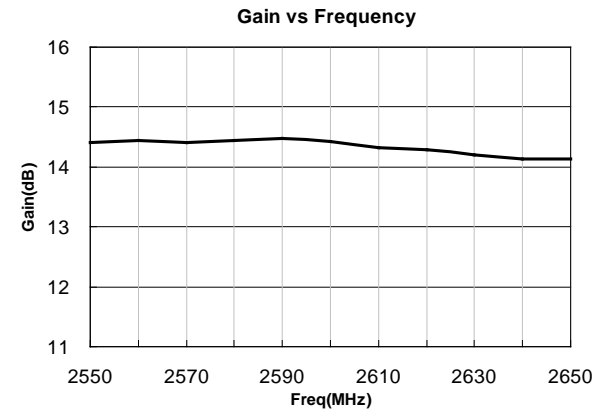
Typical Performance

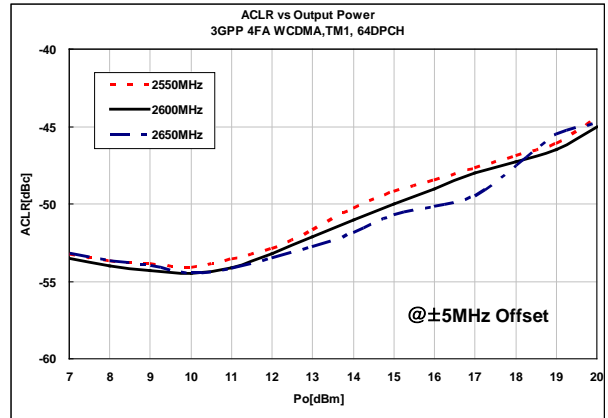
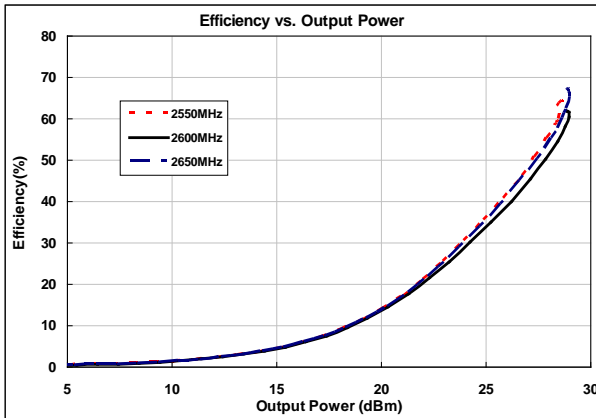
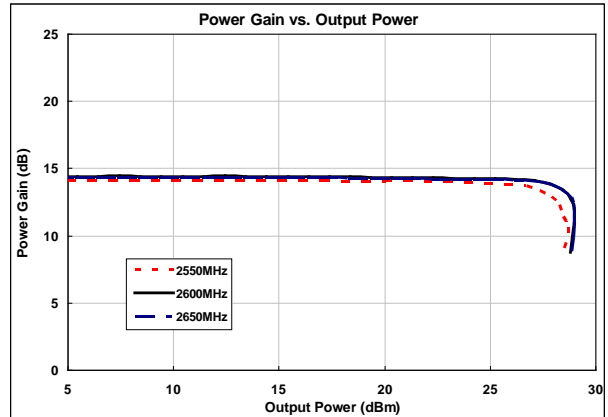
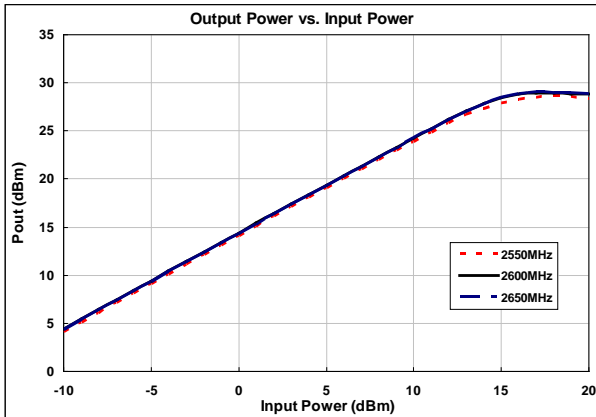
PARAMETER	UNIT	TYPICAL	REMARK
Operational Frequency Range	MHz	2550 ~ 2650	-
Small Signal Gain(S21)	dB	14.2	@ 2600MHz
Input Return Loss(S11)	dB	-15	-
Output Return Loss(S22)	dB	-17	-
Output IP3(OIP3)	dBm	38.5	@2600MHz
1dB Compression Point(P ₁ dB)	dBm	27.5	
Noise Figure(NF)	dB	3.2	-
WCDMA**	dBm	19	-
Efficiency	%	52	-
Supply Voltage/DC Current	V/mA	5/140	-

Note

- OIP3 measured with 2 tones at an output power of +10dBm/tone separated by 1MHz
- WCDMA 3G 1FA Test Model1 64DPCH, PAR = 10dB@0.01%

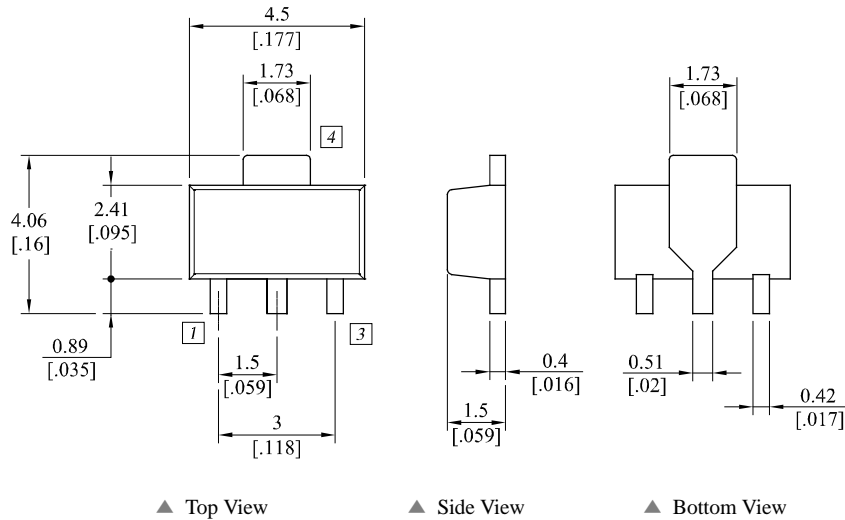
Typical Performance @ $V_{DD}=5V$, $I_{DS}=140mA$, $T=25^{\circ}C$, 50ohm System





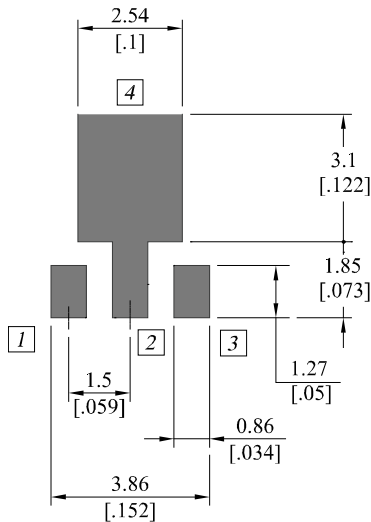
Package Dimensions (Type: SOT-89)

* Unit: mm[inch] | Tolerance ±0.2[.008]

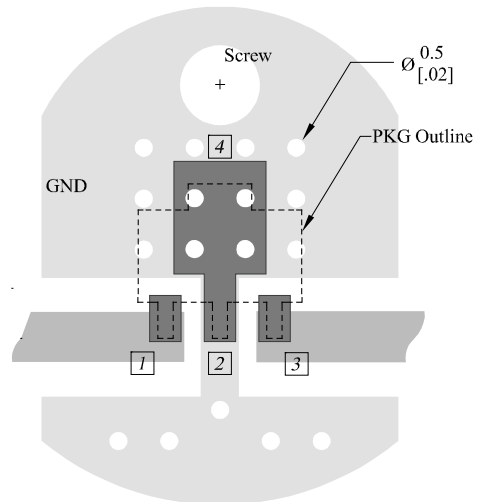


Pin Description			
Pin No	Function	Pin No	Function
1	Input	4	GND
2	GND		-
3	Output / Bias		-

Recommended Pattern



Recommended Mounting Configuration



* Mounting Configuration Notes

1. Ground / thermal via holes are critical for the proper performance of this device.
2. Add as much copper as possible to inner and outer layers near the part to ensure optimal thermal performance.
3. Mounting screws can be added near the part to fasten the board to a heatsink. Ensure that the ground / thermal via hole region contacts the heatsink.
4. Do not put solder mask on the backside of the PCB in the region where the board contacts the heatsink.
5. RF trace width depends upon the PCB material and construction.
6. Use 1 oz. Copper minimum.

Revision History

Part Number	Release Date	Version	Modification	Data Sheet Status
AE367	2012.4.11	1.3	Revision : Absolute Maximum Ratings	-
AE367	2012.10.17	1.2	New datasheet format	-
AE367	2012.4.25	1.1	FIXED : Operating Frequency Range.	-
AE367	2011.11.7	1.0	Initial Release.	-

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