

Features

- 4 dBm Output Level at 1950 MHz
- Channel Step Size : 200 kHz
- 2nd Harmonic : < -25 dBc
- Spurious Level : < -73 dBc
- 35 mA Current Consumption

Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.

Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz	1900	1950	2000
Output Power	dBm	1	4	7
Supply Voltage	V	4.75	5	5.25
Current Consumption	mA		25	35
Channel Step Size	kHz		200	
2 nd Harmonics	dBc		-25	-20
Spurious Level	dBc		-73	-68
Reference Frequency	MHz		10	
Phase Noise (C / N)				
@ 1 kHz	dBc/Hz		-80	-73
@ 10 kHz	Ω		-96	-92
@ 100 kHz			-118	-114
Operating Temp. Range			25	
Package Type & Size	°C	SMT, 15.0W×15.0L ×3.2H		

1) Measurement conditions are as follows: T = 25°C, V_{CC} = 5 V, Freq. = 1950 MHz, 50 ohm system.

2) Frequency stability of internal ref. TCXO is +/- 2.5 ppm (max.) over the operating temperature range.

More Information

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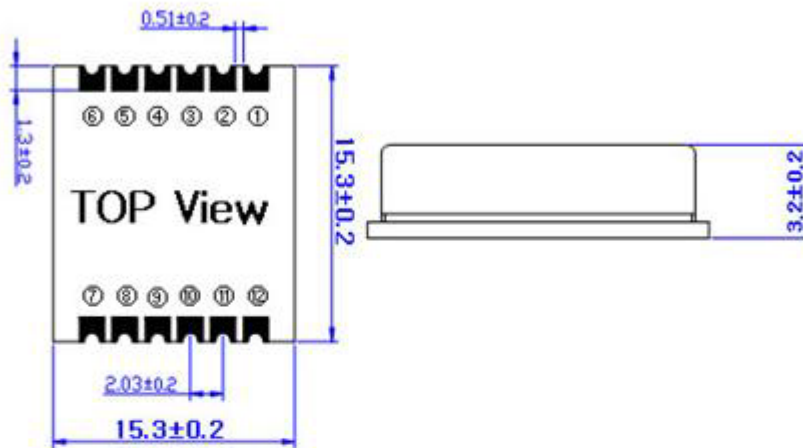
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Outline Drawing

APLT0000 Type

Type	Dimension
Standard (PLL IC + VCO)	15.3 x 15.3 x 3.2



Pin Out for PLL			
Pin No.	Application	Pin No.	Application
1	CLOCK	7	VCC (VCO)
2	DATA	10	RF OUT
3	ENABLE	11	VCC (PLL)
4	OSC IN	12	LOCK DETECT
All other Pins are Grounded			

