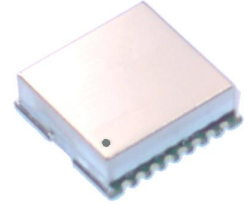


Features

- 4 dBm Output Level at 1288 MHz
- 2nd Harmonic: < -20 dBc
- Spurious Level: < -68 dBc
- Current Consumption: < 35 mA

Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



Specifications

| Parameter | Unit | Min. | Typical | Max. |
|---------------------------|--------|-----------------------|---------|------|
| Frequency Range | MHz | | 1288 | |
| Output Power | dBm | 2 | 4 | 6 |
| Supply Voltage | V | 4.8 | 5 | 5.2 |
| Current Consumption | mA | | 28 | 35 |
| Channel Step Size | kHz | | 1000 | |
| 2 nd Harmonics | dBc | | -25 | -20 |
| Spurious Level | dBc | | -72 | -68 |
| Reference Frequency | MHz | | 56 | |
| Reference Input Level | dBm | 0 | 2 | 5 |
| Phase Noise (C / N) | | | | |
| @ 1 kHz | | | -75 | -70 |
| @ 10 kHz | dBc/Hz | | -104 | -100 |
| @ 100 kHz | | | -120 | -115 |
| Output Impedance | Ω | | 50 | |
| Operating Temp. Range | °C | -30 | 25 | 80 |
| Package Type & Size | mm | SMT, 19.0W×19.0L×5.8H | | |

1) Measurement conditions are as follows: T = 25°C, V_{cc} = 5 V, Freq. = 1288 MHz, 50 ohm system.

More Information

Website: www.asb.co.kr
E-mail: sales@asb.co.kr

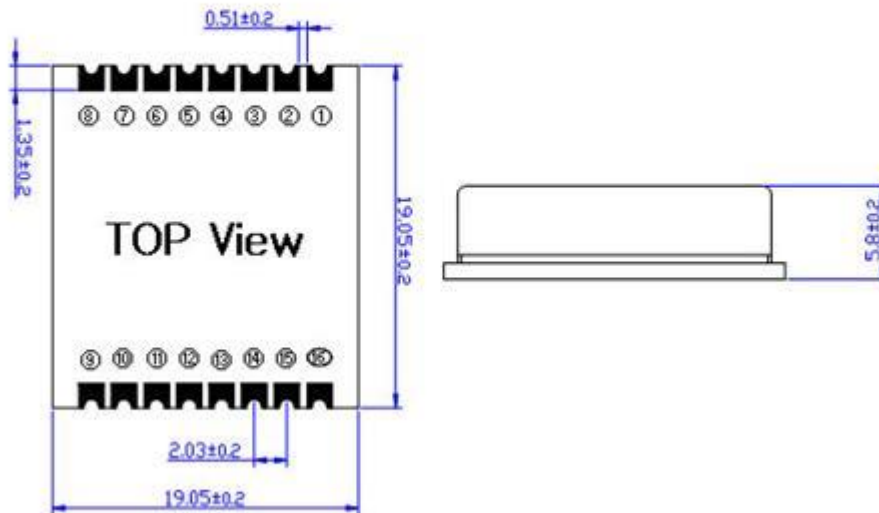
Tel: (82) 42-528-7220
Fax: (82) 42-528-7222

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Outline Drawing

APL0000-R Type

| Type | Dimension |
|--------------------|---------------|
| PLL IC + VCO + ROM | 19 x 19 x 5.8 |



| Pin Out for PLL | | | |
|--------------------------------------------|-------------|---------|-------------|
| Pin No. | Application | Pin No. | Application |
| 1 | GROUND | 9 | VCC (VCO) |
| 2 | GROUND | 13 | RF OUT |
| 3 | GROUND | 15 | VCC (PLL) |
| 4 | OSC IN | 16 | LOCK DETECT |
| All other Pins are Grounded / Internal ROM | | | |

