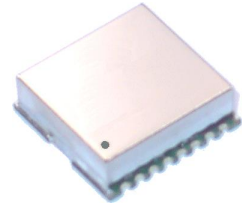


## Features

- 3 dBm Output Level at 2450 MHz
- Channel Step Size : 100kHz
- 2<sup>nd</sup> Harmonic: < -20 dBc
- Spurious Level : < -65 dBc
- Current Consumption: < 30mA

## Description

The plerow™ PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



## Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz	2400	2450	2500
Output Power	dBm	0	3	
Supply Voltage	V	4.8	5.0	5.2
Current Consumption	mA		28	40
Channel Step Size	kHz		100	
2 Harmonics (Fundamental)	dBc		-25	-20
Spurious Level	dBc		-70	-65
Reference Frequency	MHz		10	
Reference Input Level	dBm	2	4	
Phase Noise (C / N)		0	2	5
@ 1 kHz			-70	-65
@ 10 kHz	dBc/Hz		-97	-92
@ 100 kHz			-118	-113
Output Impedance	Ω		50	
Operating Temp. Range	°C	-30	25	80
Package Type & Size	mm	SMT, 19.0W×19.0L×5.8H		

1) Measurement conditions are as follows: T = 25°C, V<sub>cc</sub> = 5 V, Freq. = 2250 MHz, 50 ohm system.

## More Information

Website: [www.asb.co.kr](http://www.asb.co.kr)  
 E-mail: [sales@asb.co.kr](mailto:sales@asb.co.kr)

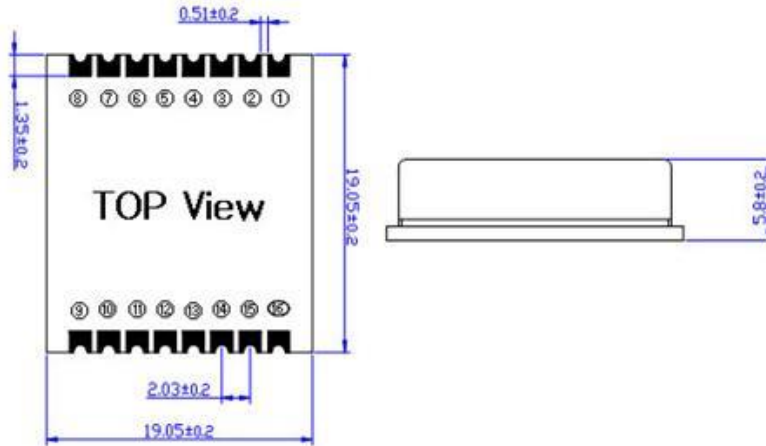
Tel: (82) 42-528-7220  
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 Bldg., 367-17 Gojeong-Dong,  
 Seo-Gu, Daejeon 302-716, Korea

### Outline Drawing

APL0000-T Type

Type	Dimension
PLL IC + VCO + TCXO	19 x 19 x 5.8



Pin Out for PLL			
Pin No.	Application	Pin No.	Application
1	CLOCK	9	VCC (VCO)
2	DATA	13	RF OUT
3	ENABLE	15	VCC (PLL)
4	GROUND	16	LOCK DETECT
All other Pins are Grounded / Internal TCXO			

