

Surface Mount

Diplexer

DPB85102-75+

75Ω DC to 1220 MHz
(DC-85, 102-1220 MHz)



CASE STYLE: PA2002

The Big Deal

- Low insertion loss, 1.3dB Typ.
- High rejection, > 50dB
- Very good return loss, 22dB Typ.
- 75Ω Impedance
- Used in DOCSIS 3.1 standard

Product Overview

DPB85102-75+ is a high performance diplexer with the lowpass port at DC-85 MHz and highpass port at 102-1220 MHz. Excellent return loss combined with high out of channel rejection makes it a ideal part in cable TV and multiband radio systems.

Key Features

Feature	Advantages
Low passband insertion loss	Passband insertion loss 1dB ensures low signal loss through both the channels.
Excellent stopband rejection	Co-channel rejection of 50dB ensures unwanted spurious are eliminated.
Excellent return loss at DC-85 and 102-1220 MHz	This makes signal transmission with very less reflection and well-matched with the adjacent component used in the system.

Notes

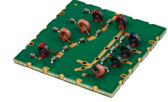
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+RoHS Compliant
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Maximum Ratings

Operating Temperature	-40° to 85°C
Storage Temperature	-55°C to 100°C
RF Power Input	29dBm Max.

Permanent damage may occur if any of these limits are exceeded. These ratings are not intended for continuous normal operation

Pin Connections

HIGH PASS PORT	7
LOW PASS PORT	9
COMMON PORT	18
GROUND	1-6,8,10-17,19,20

Features

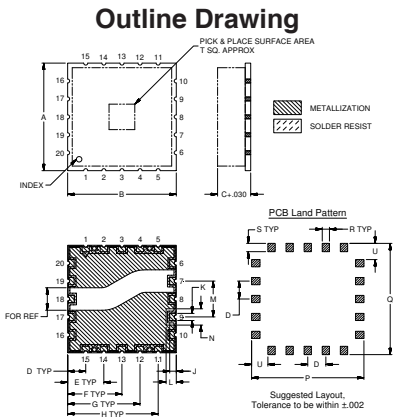
- Low insertion loss
- 75Ω Impedance
- Excellent return loss
- High rejection

Applications

- Cable TV systems (DOCSIS 3.1 standard)
- Multiband radio systems

Electrical Specifications at 25°C

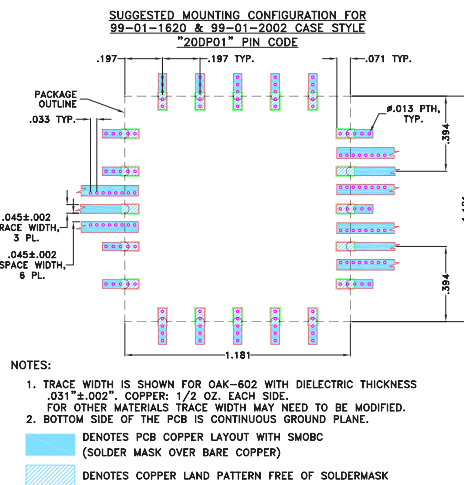
Parameter	Port	Frequency (MHz)	Min.	Typ.	Max.	Unit	
Pass Band	Insertion Loss	Low Pass	DC-85	-	1.2	1.7	dB
		High Pass	102-1220	-	1.3	1.8	
	Return Loss	Low Pass	DC-85	18	22	-	dB
High Pass		102-1220	16	22	-		
Common		DC-85	18	22	-		
Stop Band Isolation	Low Pass	102-1220	45	50	-	dB	
	High Pass	DC-85	45	50	-		



Outline Dimensions (inch/mm)

	A	B	C	D	E	F	G	H	J	K
	1.181	1.181	.300	.197	.394	.591	.787	.984	.071	.079
	30.00	30.00	7.62	5.00	10.00	15.00	20.00	25.00	1.80	2.00
	L	M	N	P	Q	R	S	T	U	Wt.
	.111	.394	.179	1.221	1.221	.079	.091	.280	.178	grams
	2.82	10.00	4.54	31.01	31.01	2.01	2.31	7.11	4.52	3.8

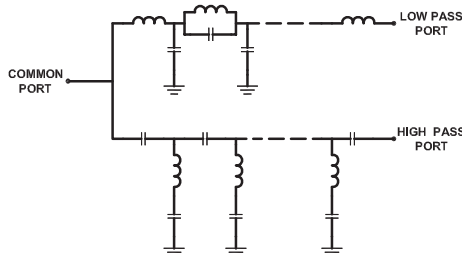
Demo Board MCL P/N: TB-786+ Suggested PCB Layout (PL-435)



Typical Performance Data at 25°C

FREQUENCY (MHz)	INSERTION LOSS (dB)			RETURN LOSS (dB)	
	Low Pass Port	High Pass Port	Common Port	Low Pass Port	High Pass Port
1.00	0.05	82.15	48.38	47.86	0.01
5.00	0.08	67.67	44.83	44.43	0.01
40.00	0.21	60.39	33.40	31.22	0.04
85.00	1.24	72.24	27.77	24.80	0.68
89.00	2.20	36.13	23.42	25.52	1.32
89.50	2.49	32.61	20.47	20.48	1.52
91.00	4.26	21.51	13.82	10.74	2.86
92.00	7.42	12.06	12.82	6.27	6.51
93.00	13.76	5.92	16.55	3.25	19.52
95.00	26.94	3.01	15.80	1.46	13.76
96.00	34.49	2.45	16.53	1.16	14.84
96.50	38.73	2.24	17.34	1.06	15.77
97.75	51.51	1.83	20.16	0.88	18.87
98.00	54.54	1.77	20.83	0.85	19.61
100.00	58.26	1.39	28.35	0.69	26.39
102.00	62.58	1.16	39.66	0.59	30.56
120.00	56.29	0.53	28.34	0.34	26.26
150.00	57.08	0.33	26.90	0.26	26.50
250.00	58.93	0.22	29.28	0.12	26.09
500.00	57.61	0.25	19.89	0.02	19.52
1000.00	55.35	0.32	20.86	0.13	21.99
1220.00	52.81	0.31	24.85	0.29	34.21

Functional Schematic

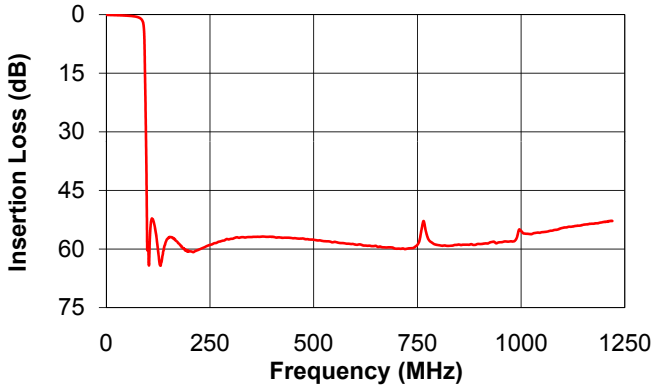


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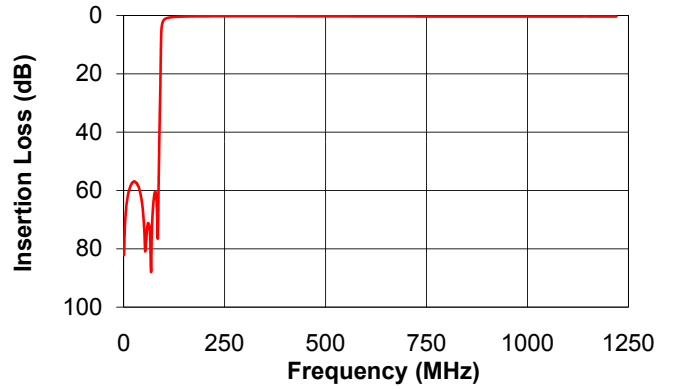
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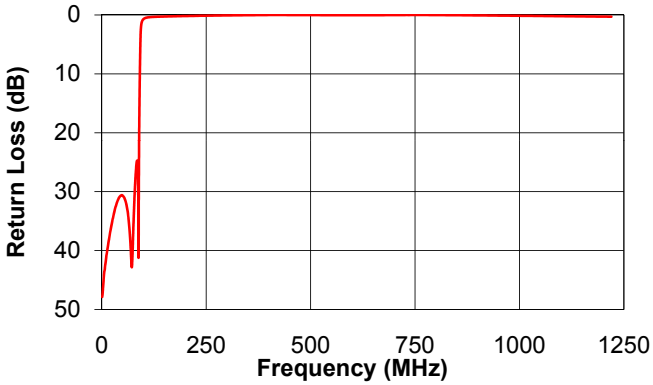
DPB85102-75+ LOW PASS PORT
INSERTION LOSS ($P_{in}=0dBm$)



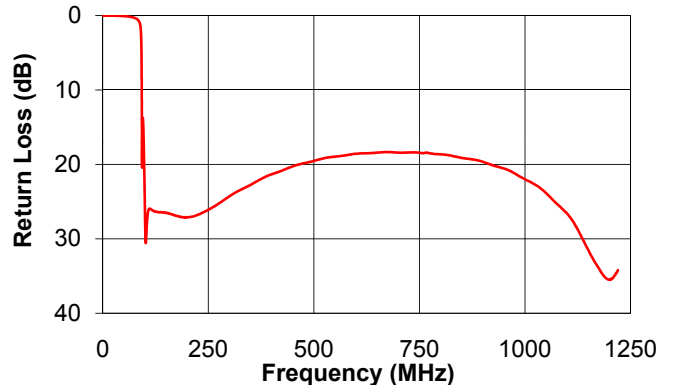
DPB85102-75+ HIGH PASS PORT
INSERTION LOSS ($P_{in}=0dBm$)



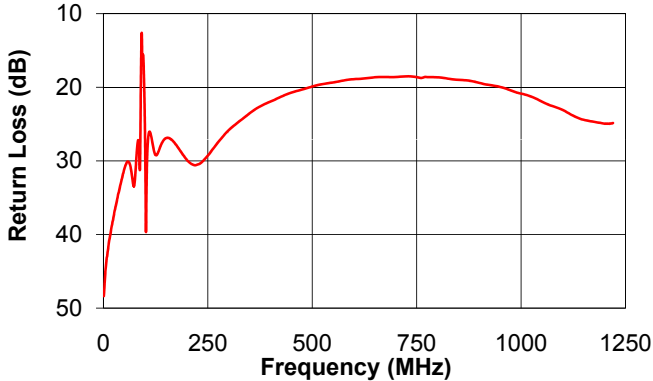
DPB85102-75+ LOW PASS PORT
RETURN LOSS ($P_{in}=0dBm$)



DPB85102-75+ HIGH PASS PORT
RETURN LOSS ($P_{in}=0dBm$)



DPB85102-75+ COMMON PORT
RETURN LOSS ($P_{in}=0dBm$)



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