
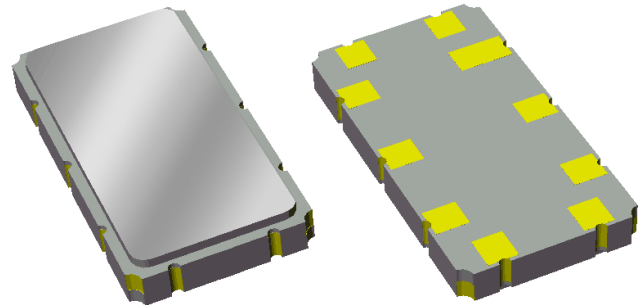


## Features

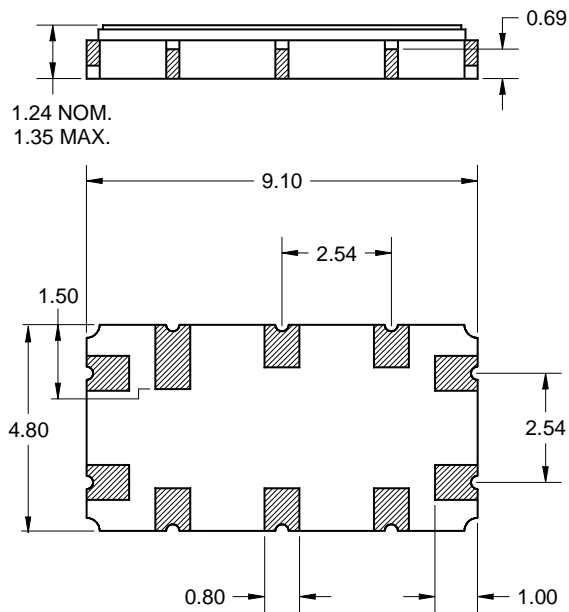
- For multiple applications
- Usable bandwidth 10 MHz
- Low loss
- High attenuation
- Balanced operation
- Ceramic Surface Mount Package (SMP)
- Hermetic
- RoHS compliant (2002/95/EC), Pb-free 



## Package

Surface Mount 9.10 x 4.80 x 1.24 mm

SMP-35C

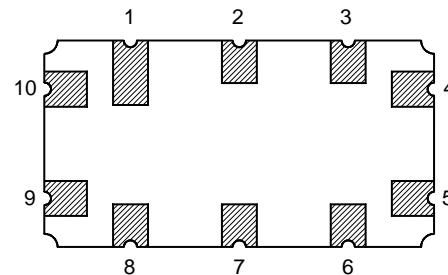


Dimensions shown are nominal in millimeters  
All tolerances are  $\pm 0.15$ mm except overall  
length and width  $\pm 0.10$ mm

Body:  $Al_2O_3$  ceramic  
Lid: Kovar, Ni plated  
Terminations: Au plating 0.5 - 1.0 $\mu$ m,  
over a 2 - 6 $\mu$ m Ni plating

## Pin Configuration

Bottom View



Pin No.	Description
9	Input +
10	Input -
4	Output +
5	Output -
1,2,3,6,7,8	Case Ground

**Electrical Specifications <sup>(1)</sup>**

**Operating Temperature Range:** <sup>(2)</sup> -40 to +85 °C

Parameter <sup>(3)</sup>	Minimum	Typical <sup>(5)</sup>	Maximum	Unit
<b>Center Frequency</b>	-	140	-	MHz
<b>Minimum Insertion Loss</b>	-	10	11.5	dB
<b>Amplitude Variation</b> 135 – 145 MHz	-	0.4	0.9	dB p-p
<b>Phase Linearity</b> 136 – 144 MHz	-	2.0	6	° p-p
135 – 145 MHz	-	2.2	8	° p-p
<b>Average Group Delay</b> 135 – 145 MHz	0.72	0.77	0.82	µs
<b>Relative Attenuation</b> <sup>(4)</sup>				
10 – 116 MHz	48	52	-	dB
116 – 125 MHz	40	45	-	dB
125 – 127.5 MHz	33	41	-	dB
152.5 – 158 MHz	31	41	-	dB
158 – 177 MHz	35	45	-	dB
177 – 280 MHz	40	50	-	dB
<b>Triple Transit Suppression</b>	30	45	-	dB
<b>Source Impedance (balanced)</b> <sup>(6)</sup>	-	50	-	Ω
<b>Load Impedance (balanced)</b> <sup>(6)</sup>	-	50	-	Ω

**Notes:**

1. All specifications are based on the TriQuint matching schematic shown on page 5
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. Relative to minimum insertion loss
5. Typical values are based on average measurements at room temperature
6. This is the optimum impedance in order to achieve the performance shown

**Electrical Specifications <sup>(1)</sup>**

**Operating Temperature Range:** <sup>(2)</sup> -20 to +85 °C

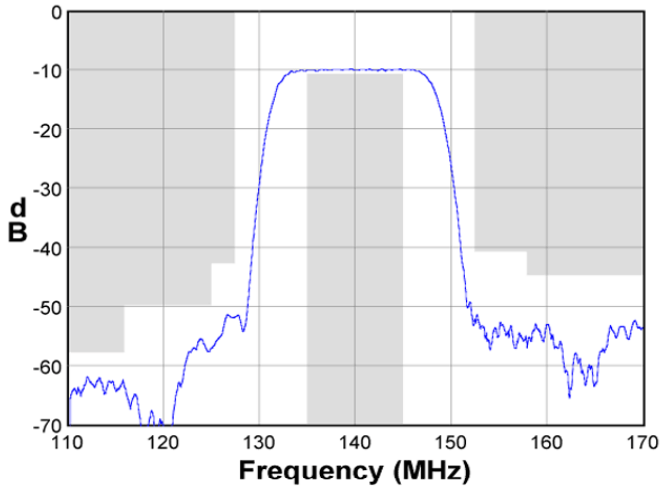
Parameter <sup>(3)</sup>	Minimum	Typical <sup>(5)</sup>	Maximum	Unit
<b>Center Frequency</b>	-	140	-	MHz
<b>Minimum Insertion Loss</b>	-	10	11.5	dB
<b>Amplitude Variation</b> 135 – 145 MHz	-	0.4	0.9	dB p-p
<b>Phase Linearity</b> 136 – 144 MHz	-	2.0	4	° p-p
135 – 145 MHz	-	2.2	8	° p-p
<b>Average Group Delay</b> 135 – 145 MHz	0.72	0.77	0.82	µs
<b>Relative Attenuation</b> <sup>(4)</sup> 10 – 116 MHz	48	52	-	dB
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<b>Load Impedance (balanced)</b> <sup>(6)</sup>	-	50	-	Ω

**Notes:**

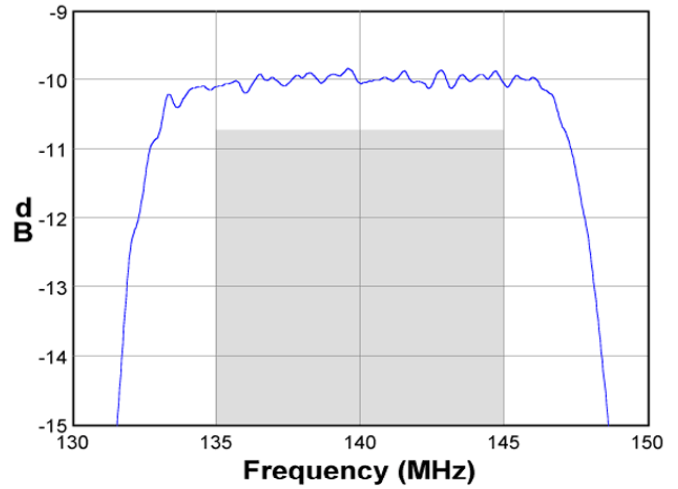
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6. This is the optimum impedance in order to achieve the performance shown

**Typical Performance (at room temperature)**

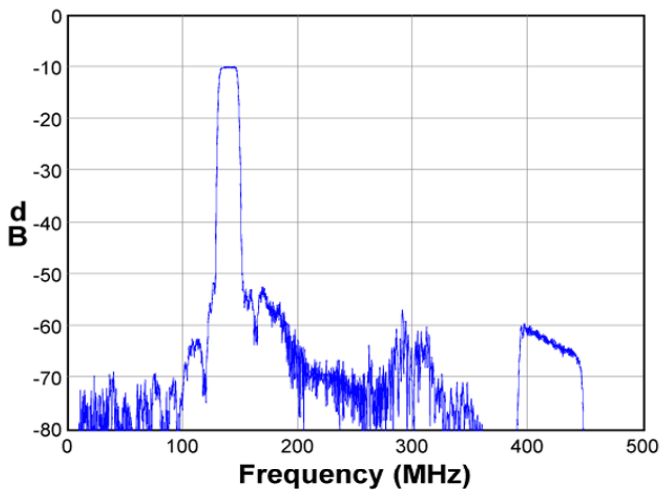
**Frequency Response**



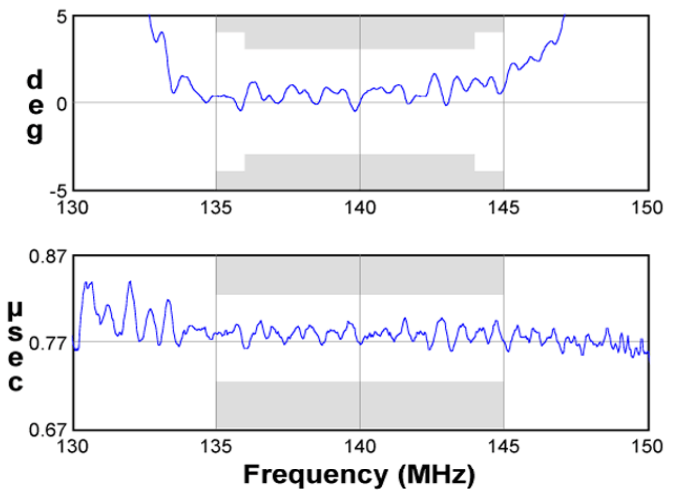
**Passband Response**



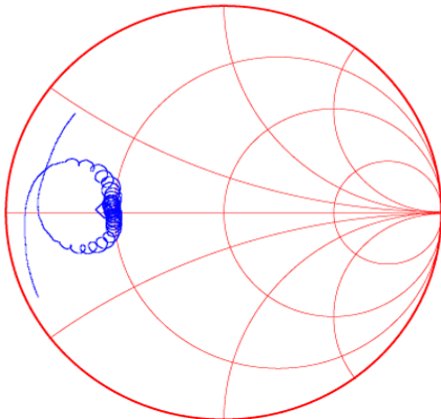
**Wideband Response**



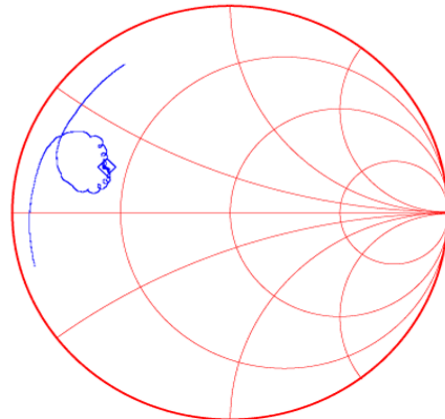
**Phase / Group Delay**



**Input Smith Chart**

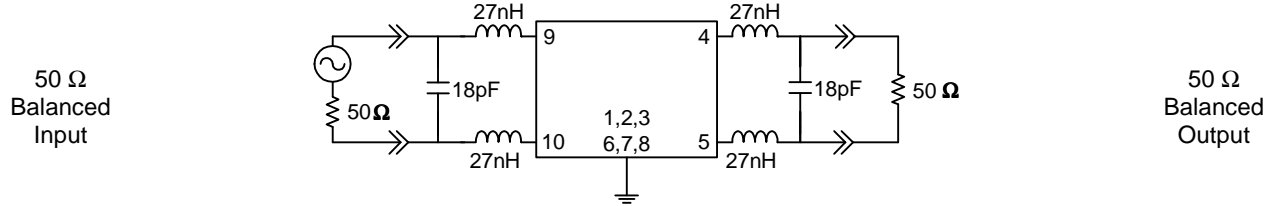


**Output Smith Chart**

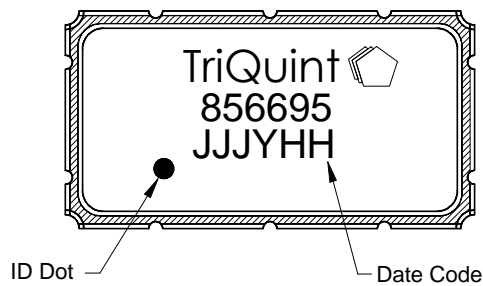


**Matching Schematic**

Actual matching values may vary due to PCB layout and parasitics

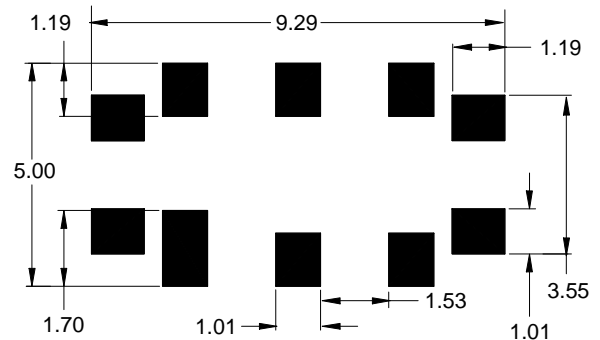


**Marking**



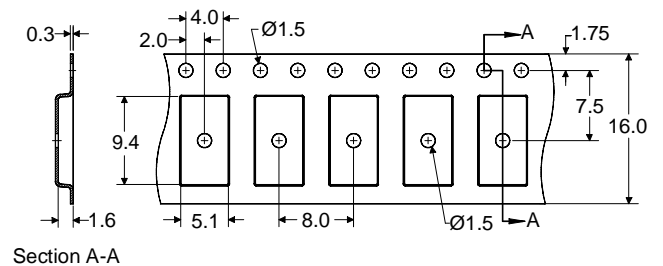
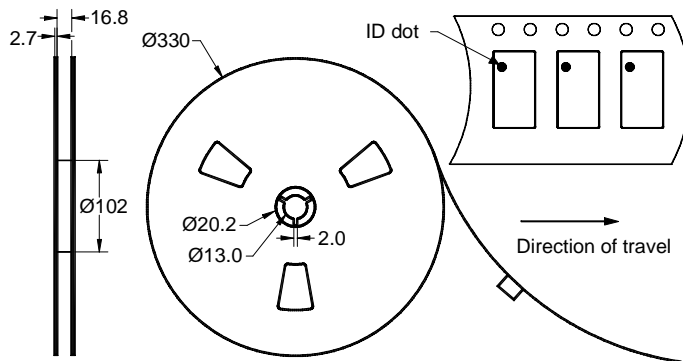
The date code consists of: day of the current year (Julian, 3 digits), last digit of the year (1 digit) and hour (2 digits)

**PCB Footprint**



This footprint represents a recommendation only  
Dimensions shown are nominal in millimeters

**Tape and Reel**




Dimensions shown are nominal in millimeters  
Packaging quantity: 4000 units/reel

### Maximum Ratings


Parameter	Symbol	Minimum	Maximum	Unit
Operating Temperature Range	T	-40	+85	°C
Storage Temperature Range	T <sub>stg</sub>	-55	+125	°C
Pyroelectric Voltage	V <sub>Pyro</sub>	-	50	mV p-p
Input Power	P <sub>in</sub>	-	+20	dBm

### Important Notes

#### Warnings

- Electrostatic Sensitive Device (ESD) 
- Avoid ultrasonic exposure

#### RoHS Compliance

- This product complies with EU directive 2002/95/EC (RoHS) 

#### Solderability

- Compatible with JEDEC J-STD-020C **Pb-free** process, **260°C** peak reflow temperature ([see soldering profile](#))

### Links to Additional Technical Information

[PCB Layout Tips](#)

[Qualification Flowchart](#)

[Soldering Profile](#)

[S-Parameters](#)

[RoHS Information](#)

[Other Technical Information](#)

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