# OUTPUTS Output Frequency Level (into 50Ω) A 10 MHz $+13 \pm 2$ dBm B 4 GHz $+13 \pm 2$ dBm STABILITY Aging 1 x 10<sup>-7</sup> first year after 30 days operating, typical $5 \times 10^{-8}$ second year, typical

# 2 x 10<sup>-8</sup> per year thereafter, typical Phase Noise L(f), dBc/Hz, typical

	10 MHz	4 GHz
10 Hz	-140	-86
100 Hz	-160	-104
300 Hz	-165	-109
1 kHz	-172	-122
10 kHz	-174	-139
100 kHz	-175	-141

# **Temperature Stability**

 $\pm 5 \times 10^{-9}$ , 0 to  $+50^{\circ}$ C (Ref.  $+25^{\circ}$ C)

#### **Harmonics**

≤ -25 dBc

# **Sub-Harmonics**

≤ -60 dBc

#### PLL Reference Products

≤ -60 dBc

## **Spurious**

≤ -80 dBc, excluding power supply line related spurs

#### **Phase Lock Alarm**

TTL

Locked: +3.5 VDC to +5.2 VDC (Hi) Out-of-Lock: +0.8 VDC max (Lo)

# **Phase Lock Voltage Monitor**

Voltage monitor pin supplied

## **MECHANICAL**

#### **Dimensions**

7.46 x 4 x 1"

#### Connectors

RF Outputs: SMA(f)

Power, Monitoring: Feed Thru Terminals

**GND: Ground Turret** 

# **Packaging**

Nickel-plated machined aluminum housing – J3PMX

#### Mounting

Threaded inserts on base, #2-56, 11 places

#### POWER REQUIREMENTS

#### **Warm-Up Power**

≤ 23.5 Watts for 5 minutes

#### **Total Power**

≤ 16.5 Watts at +25°C

# Supply Voltage

+15 VDC ±5%

#### **ADJUSTMENT**

Mechanical Tuning (Internal 10 MHz)

±1 x 10<sup>-6</sup>

Loop BW (Internal 100 MHz PLL)

Target Bandwidth: ~250 Hz

Type 2 Loop

#### **CRYSTAL**

#### **Type**

100 MHz SC-cut (x40)

# OTHER

#### Label

Use conventional label with the following information:

501-25791 (Current Rev.)

10M/4G MXO-PLMX

+15 VDC

Serial # - Date Code

(Mark connectors with function)

#### **Test Data**

- Output Level
- Phase Noise
- Temperature Stability
- Harmonics, Subs, Products, Spurs
- Power Warm-up and Total

REV	DATE	REVISION RECORD	DWN	AUTH
-	07-23-13	Initial Release	PAC	

J3PMX MXO Connections		
Function		
Supply Voltage		
Ground, Case		
RF Output B		
Phase Lock Voltage		
Phase Lock Alarm		
RF Output A		



