



ROHS-Compliant Product

O-6000SC Series



1. Specification		
Frequency range:	5.0 ... 100.0 MHz	
Type:	O-6500SC	O-6300SC
Supply voltage V_S :	+5.0 V \pm 5 %	+3.3 V \pm 5 %
Frequency stability vs. temperature options: $\leq \pm 5 \times 10^{-9}$ vs. 0 °C to +50 °C: $\leq \pm 1 \times 10^{-8}$ vs. -10 °C to +60 °C: $\leq \pm 1 \times 10^{-8}$ vs. -20 °C to +70 °C: $\leq \pm 3 \times 10^{-8}$ vs. -40 °C to +85 °C: $\leq \pm 5 \times 10^{-8}$ vs. -40 °C to +85 °C:	655x 656x 657x 658x 659x	635x 636x 637x 638x 639x
Aging stability option (after 30 days of operation) $\leq \pm 1 \times 10^{-9}$ / day; $< \pm 1 \times 10^{-7}$ / year: $\leq \pm 2 \times 10^{-9}$ / day; $< \pm 1 \times 10^{-7}$ / year: $\leq \pm 5 \times 10^{-10}$ / day; $< \pm 5 \times 10^{-8}$ / year:	65x1 65x2 65x3	63x1 63x2 63x3
Frequency stability vs. supply voltage changes $V_S \pm 5 \%$: vs. load changes $\pm 5 \%$:	$\leq \pm 5.0 \times 10^{-9}$ $\leq \pm 1.0 \times 10^{-9}$	
Frequency control by external voltage 0 V ... V_{REF} :	$\geq \pm 1$ ppm	
Linearity:	$\leq 10 \%$	
Reference Voltage V_{REF} :	+4.0 V \pm 5 %	+3.0 V \pm 5 %
Power consumption @ +25 °C steady state: during warm-up:	≤ 1.5 W ≤ 3.5 W	
Warm-up time: (for a typical accuracy of $\leq \pm 10$ ppb @ +25 °C referred to final frequency after 1 hour)	≤ 5 min	
Output voltage / Load Option H : Option S :	(LV)HCMOS / 1 kOhm // 15 pF Sinewave / $\geq +3$ dBm / 50 Ohm	
Phase noise 10 Hz: 100 Hz: 1 kHz: 10 kHz:	(typical for 10 MHz) -110 dBc / Hz -130 dBc / Hz -145 dBc / Hz -155 dBc / Hz	
Storage temperature range:	-45 °C ... +90 °C	

4				KVG Quartz Crystal Technology GmbH P.O.Box 61 D-74924 Neckarbischofsheim Tel. +49 (0) 7263 / 648-0 Fax. +49 (0) 7263 / 6196
3				
2				
1		24.08.07	M. Zupan	
ED	Description	Date	Name	



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2. Environmental conditions

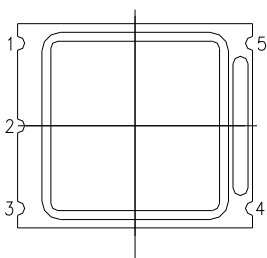
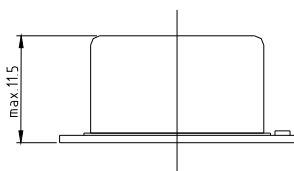
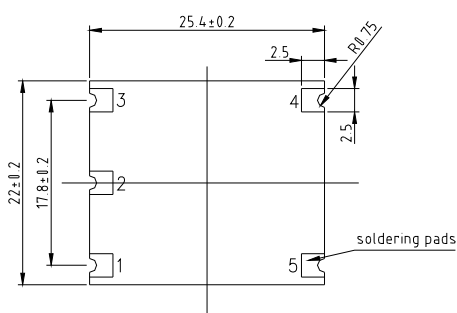
According to KVG Product Qualification Procedure AA-QM-200

3. Marking

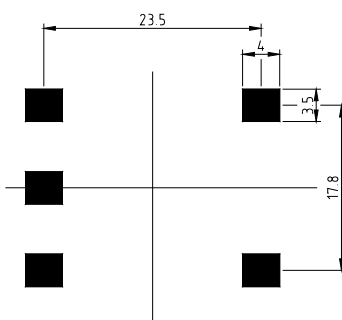
Manufacturer's name, date code(week/year); Specification; Center frequency

4. Case

BF144-11.5-SMD



Foot print for PCB Design

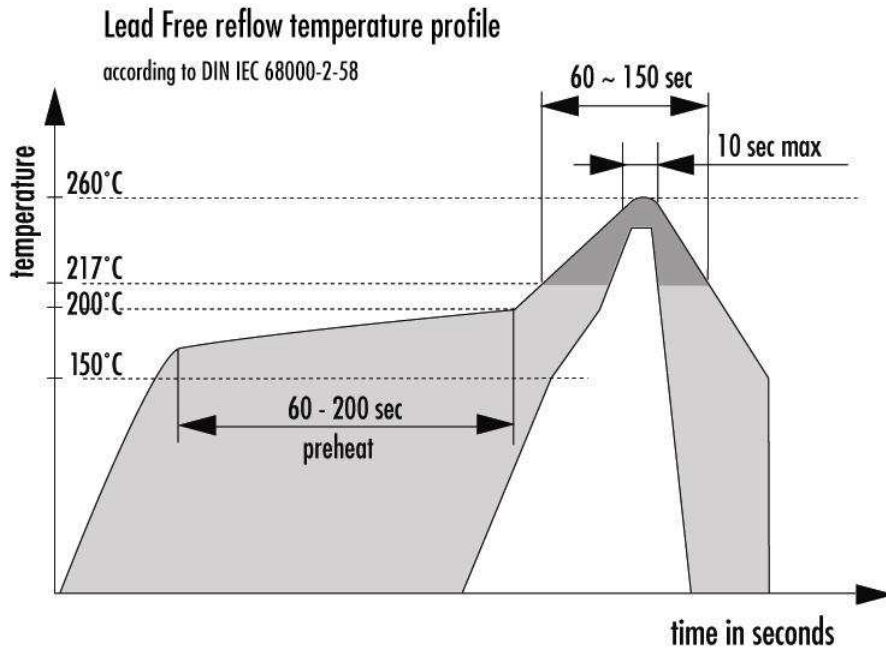


1.Pin configuration

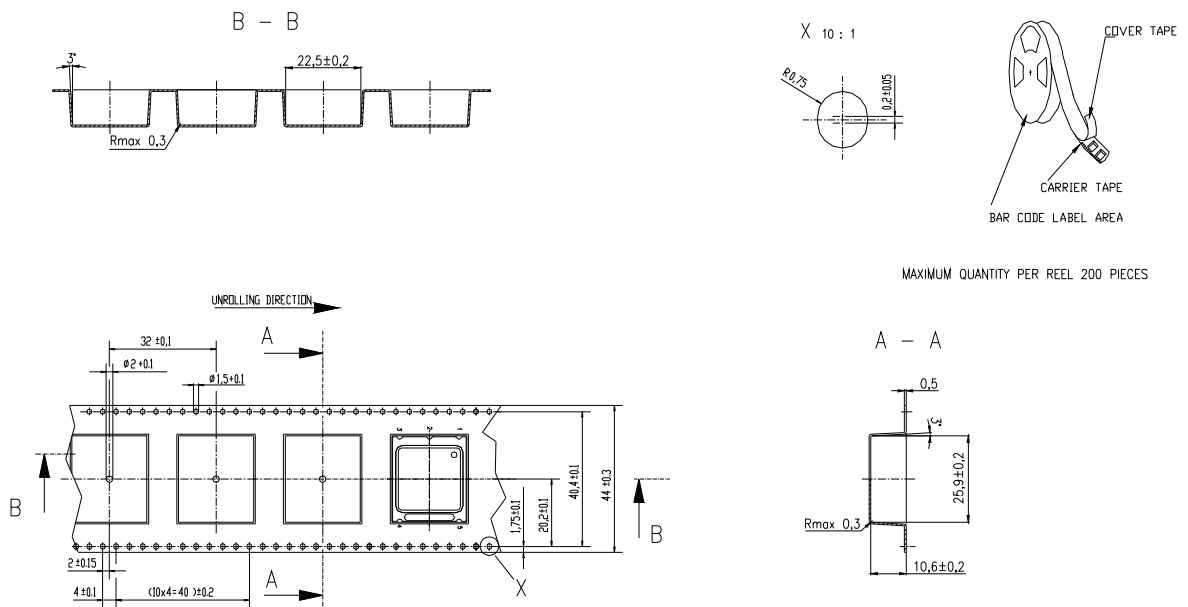
1. Control voltage V_C
2. Reference voltage output V_{REF}
3. Supply voltage V_S
4. RF-output
5. Ground, case

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5. Recommended soldering profile



6. Tape and reel



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